## Capacitance-voltage characteristics based on indium antimonide MIS-structures study

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Received 9.02.2023; revised 20.03.2023; accepted 28.03.2023

The work is devoted to study of capacitance-voltage (CV) characteristics metal-insulatorsemiconductor (MIS) structures, fabricated from single-crystal indium antimonide plates cut along the (211) and (100) planes from an ingot grown in the [211] direction and along the plane (100) from an ingot grown in the [100] direction. MIS structures were of two types: with an anode oxide layer and with a protective dielectric composition, including additional layer of SiO<sub>x</sub>. Measurements of the CV curve of MIS structures flooded with liquid nitrogen were carried out with multiple forward (starting from zero bias at the field electrode) and reverse "passes" at a bias voltage change rate 500 mV/s. Analysis results of "passes" made it possible to determine the sign and values of the effective surface charge in the initial state and after each direct "pass" when additional charges of two types are detected: stable and unstable. The sign of these charges is opposite to the polarity of the voltage on the field electrode. Stable charges are unchanged during the entire cooling time and "drain" only when the MIS structure is "warmed up". Unstable ones "drain" already when the cooled MIS structure is short-circuited.

It is shown that the values of all types of surface charge, and hence the concentration of traps, are minimal in the case of plates cut along the (100) plane from ingots grown in the [100] direc

*Keywords*: capacitance-voltage characteristic, MIS-structure, indium antimonide, effective surface charge.

DOI: 10.51368/2307-4469-2023-11-2-146-154

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